REMARKS

Reconsideration of this application, as amended, is respectfully requested.

Claims 1-20 are pending. Claims 1-20 stand rejected.

Claims 1, 3, 7, 9, 11, 14, 15, and 18 have been amended. No claims have been canceled. No claims have been added. Support for the amendments is found in the specification, the drawings, and in the claims as originally filed. Applicant submits that the amendments do not add new matter.

Claims 1-3, 6-10, and 15-19 have been rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Publication No. 2002/0057265 to Tamura et al. ("Tamura").

Amended claim 1 reads as follows:

An article comprising:
a storage medium comprising machine-readable instructions stored thereon to:
execute a software driver for a display codec, the software driver configured to work with a plurality of display codecs while remaining in a default configuration; and transmit digital signals from the display codec to a display using the software driver.

(emphasis added)

Tamura discloses a display driver capable of implementing natural moving mage display. More specifically, Tamura discloses:

In the electronic instrument 500 having the above configuration, in the case where the received data in which a signal received by the antenna 544 through a wireless communications network is decoded by the codec circuit 548 is the multiplexed compressed data, the multiplexer/demultiplexer circuit 508 demultiplexes the received data into compressed data corresponding to each medium as bitstream data, and supplies the data to the corresponding output interface ICs.

(Tamura, paragraph [0214], Figure 8)(emphasis added)

In particular, Tamura discloses:

For example, the display driver IC with a built-in RAM 512 as an input interface

> IC decodes the compressed video data demultiplexed by the
> multiplexer/demultiplexer circuit 508 every {fraction (1/15)} of a second using
> the FIFO memory as shown in FIG. 1 or 6, and writes the decoded data into the
> display data RAM so that the write operations performed at a higher speed
> precede the read operations. This prevents the display data in the old and new
> frames read every {fraction (1/60)} of a second from being present in the built-in
> display data RAM.

(Tamura, paragraph [0215], Figure 8)(emphasis added)

Thus, Tamura merely discloses the display driver that decodes the compressed video data from the display codec. In contrast, amended claim 1 refers to a software driver configured to work with a plurality of display codecs while remaining in a default configuration.

Because Tamura fails to disclose all limitations of amended claim 1, applicants respectfully submit that claim 1 is not anticipated by Tamura under 35 U.S.C. § 102(b).

Given that claims 2-3, 6-10, and 15-19 contain the limitations that are similar to those limitations discussed with respect to amended claim 1, applicants respectfully submit that claims 2-3, 6-10, and 15-19 are not anticipated by Tamura under 35 U.S.C. § 102(b).

Claim 4 has been rejected under 35 U.S.C. § 103(a) as being unpatentable over Tamura in view of U.S. Publication No. 2005/0104899 to Swartz et al. ("Swartz").

As set forth above, Tamura fails to disclose a software driver configured to work with a plurality of display codecs while remaining in a default configuration, as recited in amended claim 1.

Swartz, in contrast, discloses a real time stream processor, and also fails to disclose such limitations of amended claim 1.

Thus, neither Tamura, nor Swartz discloses, teaches, or suggests a software driver configured to work with a plurality of display codecs while remaining in a default configuration, as recited in amended claim 1.

It is respectfully submitted that Tamura does not teach or suggest a combination with Swartz, and Swartz does not teach or suggest a combination with Tamura. It would be impermissible hindsight based on Applicant's own disclosure, to combine Tamura and Swartz.

Furthermore, even if Tamura and Swartz were combined, such a combination would lack a software driver configured to work with a plurality of display codecs while remaining in a default configuration, as recited in amended claim 1.

Given that claim 4 depends from amended claim 1, applicants respectfully submit that claim 4 is not obvious under 35 U.S.C. § 103(a) over Tamura in view of Swartz.

Claims 5, 11-14, and 20 have been rejected under 35 U.S.C. § 103(a) as being unpatentable over Tamura in view of U.S. Publication No. 2005/0155043 to Schulz et al. ("Schulz").

As set forth above, Tamura fails to disclose a software driver configured to work with a plurality of display codecs while remaining in a default configuration, as recited in amended claim 1.

Schulz, in contrast, discloses a human-machine interface and a method to remotely monitoring and controlling machine, and also fails to disclose such limitations of amended claim 1.

Thus, neither Tamura, nor Schulz discloses, teaches, or suggests a software driver configured to work with a plurality of display codecs while remaining in a default configuration, as recited in amended claim 1.

It is respectfully submitted that Tamura does not teach or suggest a combination with Schulz, and Schulz does not teach or suggest a combination with Tamura. It would be impermissible hindsight based on Applicant's own disclosure, to combine Tamura and Schulz.

Furthermore, even if Tamura and Schulz were combined, such a combination would lack a software driver configured to work with a plurality of display codecs while remaining in a default configuration, as recited in amended claim 1.

Given that claims 5, 11-14, and 20 contain the limitations that are similar to those limitations discussed with respect to amended claim 1, applicants respectfully submit that claims 5, 11-14, and 20 are not obvious under 35 U.S.C. § 103(a) over Tamura in view of Schulz.

It is respectfully submitted that in view of the amendments and arguments set forth herein, the applicable rejections and objections have been overcome. If there are any additional charges, please charge Deposit Account No. 022666 for any fee deficiency that may be due.

Respectfully submitted,

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